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L9	18	("3484984" | "3696548" | "4348191" | "4552541" | "4556393" | "4743202" | "5013047" | "5154615" | "5411428" | "5742486" | "5779515" | "5788553" | "5853327" | "5906369" | "6190174" | "6206745" | "6227931").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/06 10:27
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L16	667	(baseboard breadboard) and sensor and component	US-PGPUB; USPAT	OR	ON	2006/01/06 10:27
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		Results
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6.	((pub-date > 1959 and pub-date < 2002 and TITLE-ABSTR-KEY((baseboard or breadboard))) and model) and component *[All Sources(- All Sciences -)]*	26
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4.	pub-date > 1959 and pub-date < 2002 and TITLE-ABSTR-KEY((baseboard or breadboard)) *[All Sources(- All Sciences -)]*	67
3.	((pub-date > 1959 and pub-date < 2002 and FULL-TEXT(virtual model) and FULL-TEXT(component)) and display) and sensor *[All Sources(- All Sciences -)]*	20
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#3	((virtual model<and>display)) <and> (pyr >= 1951 <and> pyr <= 2001)	101
#4	(((model<and>sensor)<and>component) <and> (pyr >= 1951 <and> pyr <= 2001)) <AND> (((mount*<and>structure)<and>(baseboard<or>breadboard)) <and> (pyr >= 1951 <and> pyr <= 2001))	52
#5	((((model<and>sensor)<and>component) <and> (pyr >= 1951 <and> pyr <= 2001)) <AND> (((mount*<and>structure)<and>(baseboard<or>breadboard)) <and> (pyr >= 1951 <and> pyr <= 2001))) <AND> (((virtual model<and>display)) <and> (pyr >= 1951 <and> pyr <= 2001))	0
#6	((virtual model<and>display)<and>sensor<and>component) <and> (pyr >= 1951 <and> pyr <= 2001)	24
#7	((virtual model<and>display)<and>magnet*<and>component) <and> (pyr >= 1951 <and> pyr <= 2001)	16
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a bus or a mesh (see Fig. 1)Whereas this physical **model** dominates, disagreement exists as to the
We also compare our approach with simulation and **breadboard** prototyping. Keywords: Field-Programmable Gate
approaches have been taken to verify a design: **breadboard** prototyping and software simulation. A
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a bus or a mesh (see Fig. 1)Whereas this physical **model** dominates, disagreement exists as to the
approaches have been taken to verify a design: **breadboard** prototyping and software simulation. A
them was valid only for the particular hardware. **Breadboard** prototypes are also extensively used in
usc.edu/pub/CENG/1994/CENG-94-15.ps.Z

Yearly operation profile of the ATLAS SCT detectors - January Kondo (Correct)
i T 2 E g 2k B T -exp 3 -1.5 Module **model** Two designs of the ATLAS barrel module are
difference of the design is the shape of the TPG **baseboard** as shown in Figure 3 and Figure 4. 1.6 Thermal
atlas.kek.jp/~kondo/sct/INDET-NO-203.ps

Modeling Analog Circuitry With VHDL - Mueller (1994) (Correct)
Modeling Analog Circuitry With Vhdl A Thesis Submitted
www.cse.nd.edu/pub/Reports/1994/tr-94-40.ps.gz

A Floor Boundary Sensor for Autonomous Robot Navigation - Bellutta And (Correct)
vector ffl control and prediction module ffl system **model**. The image measurements are measurements, for
the white of the floor and the black of the **baseboard**. In a different environment, a somewhat 4
alpha-bits.ai.mit.edu/people/bellutta/cv/fbs.e.ps.Z

Thermal simulation of the ATLAS SCT barrel module 27/5/97 - Thermal Simulation (Correct)
safety margin for the thermal runaway. 1. Module **models** Eight different module geometries as listed in
RAL in mid March, 1997 **model**-1 BeO picture-frame **baseboard model**-2 **model**-1 but with a longer PG basebaord
atlas.kek.jp/~kondo/sct/INDET-NO-201.ps

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
7 CyPhone—bringing augmented reality to next generation mobile phones ☐
Tino Pyssysalo, Tapio Repo, Tuukka Turunen, Teemu Lankila, Juha Röning
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8 ☐
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Leigh, Kathy Ryall, Eddie Sullivan, Jonathan S. Yedidia
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